









Session 6: Combined techniques

Chairman Kevin Sanchez, CNES Toulouse

14:30-14:50	Design analysis in analog signal circuits enhanced by Emission Microscopy and laser based techniques (Christof Brillert, INFINEON Munich)
14:50-15:10	Failure Diagnosis by Optical Techniques Combined to Layout Localization Software for Wafer Yield Improvement (Lionel Forli, ATMEL Rousset)
15:10-15:30	Multiple FA techniques on advanced technologies (Sylvain Dudit, ST Crolles)
15:30-15:50	Optical investigation of a resistance-change memory device (Fabio La Matina, EMPA)
15:50-16:10	Questions and answers (Franck Zachariasse, NXP, Nijmegen)