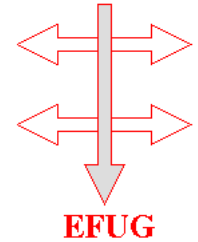


Introduction



Name: Bert Otterloo (*Bert.Otterloo@philips.com*)
Organisation: Philips Semiconductors, PMO department (Central lab)
Location: Nijmegen the Netherlands
Nijmegen site: 5 Foundries, ~ 5000 employees
Function: Groupleader fault Localisation and Analysis
Participate in: EFUG (European FIB user group)

Organisation of FA activities at Philips SC

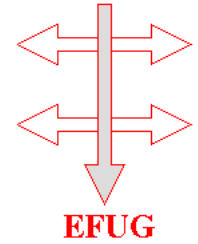
- Organisation started bottom-up in 1998 driven by FA problems encountered in deep sub micron processes (<0.35 μm /5 ML).
- Yearly Philips SC FA meeting is organised.
- FA board established with representatives from all Philips SC sites.
- FA roadmap has been written.
- Position of an FA co-ordinator approved, however currently no funding.
- Trying to embed FA board in Philips SC organisation.
- Development of FA techniques must be synchronised and integrated into the Philips SC technology roadmap

Let's make things better.



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European FIB User Group History



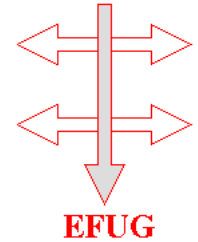
- Start Monday before ESREF 1996 in Enschede.
 - Successful FIB tutorial/workshop organised by J.Whitney and E.Demm.
- Success of meeting leads to formation of FIB user group similar to the American model of the ISTFA.
 - Requirement: Real user meeting
- ESREF 1997 in Arcachon (EFUG meeting organised by Ernst Demm Infineon).
 - Installation of EFUG board with representatives of European Semicon industry Hugo Bender(IMEC), Ronny Camp (Alcatel), Dick Verkleij (Philips), Ernst Demm(Infineon)
- ESREF 1998 in Copenhagen (EFUG meeting organised by Dick Verkleij Philips)
- ESREF 1999 in Arcachon (EFUG meeting organised by Hugo Bender IMEC).
 - EFUG web-site.
 - Best poster award.
 - Tutorial.
- ESREF 2000 in Dresden (EFUG meeting organised by Hugo Bender IMEC)
 - Over 100 attendees to EFUG meeting.
- ESREF 2001 in Arcachon (EFUG meeting organised by Geoffroy Auvert ST)

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EFUG organisation and activities



- Currently ~ 200 members
- Membership is free for members actively involved in FIB activities
- EFUG WEB site available www.imec.be/6/efug
- Current EFUG board members
 - Geoffroy Auvert (ST), Hugo Bender (IMEC), Bert Otterloo (Philips), Siegfried Pauthner (Infineon), Remy Vanden Berghe (Alcatel).
- Yearly EFUG meeting Monday prior to ESREF meeting.
 - Oral presentations including tutorial.
 - Poster sessions.
 - Best poster award.
 - Informal environment to exchange information and ideas.
 - EFUG conference dinner (sponsored by FIB manufacturers).
- Funding
 - EFUG meeting and EFUG diner sponsored by FIB manufacturers.
 - Other costs funded by employers of EFUG board members.

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