Attachment D: Application Form

Last name: Jacob

First name: Peter

Company: (1) EMPA Swiss Federal Laboratories for Materials Testing and Research, CH-8600 Duebendorf Switzerland
(2) EM Microelectronic Marin SA, CH-2074 Marin-Epagnier

Position within the company: Principal Engineer Failure Analysis

Preferred EUFANET Board of Directors Position: no preference, but neither president nor webmaster

Candidate short resume:

Peter Jacob received his engineer's degree in in Technical Physics in 1981 and joined IBM semiconductor plant Sindelfingen (GER) where he worked 10 years in semiconductor F/A. 1992 he joined Hitachi's electron microscope division (Dusseldorf, GER) for customer training& sales support. 1993 he started working at ETH Zurich (CH) as a principal engineer for both ULSI and power semiconductor F/A and reliability engineering. The group has been transfered to EMPA Duebendorf (CH) in 1997. Since 1995 he is also working additionally on a 50% shared base for EM Microelectronic Marin SA (CH), the only swill low power CMOS fab, as an F/A principal engineer. In the last 15 years, P. Jacob published about 30 articles or tutorials considering F/A subjects and is giving lectures at TU Munich and MNT Euregio Bodensee. In 2007, he was appointed to Honorary Professor at Technical University Munich

Vision of EUFANET's future:

EuFANet should become a network with an included database for difficult FA case studies. Everyone should get access on a point-based system: Uploading own contributions earns points, downloading of other contributions costs points. Furthermore, Eufanet should become more professional. I am not convinced that for the medium-term future, EuFANet can survive w/o professional supports like secretary/ webmaster and maybe a 50% position of the president. Beyond these two vision, my third vision is to extend the scope of our clients. Since classical semiconductor FA decreases more and more with the decreasing semiconductor fabrication in Europe, electronic applications increase. We must become more open to non-semiconductor experts in circuitry and PCB design, helping them for example in cases of

customer-returns or circuitry/ system-related F/A. Furthermore, EuFANet should play a more active role in definitions of F/A related methods and standards like for instance 8D-Reporting issues.

Please send this application form electronically to: election@eufanet.org
Please attach a picture ID

2